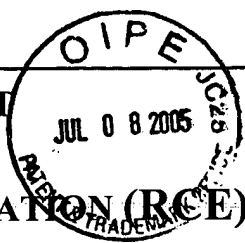


Doc # 2111



**REQUEST  
FOR  
CONTINUED EXAMINATION (RCE)  
TRANSMITTAL**

**MAIL STOP RCE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000,  
provides for continued examination of an utility or  
plant application filed on or after June 8, 1995

Application Number	10/669,655
Confirmation Number	5578
Filing Date	September 25, 2003
First Named Inventor	Yasushi KINOSHITA
Group Art Unit	2811
Examiner Name	Samuel A. GEBREMARIAM
Matter Number	Q77597
Title	SEMICONDUCTOR INTEGRATED CIRCUIT

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

**1. SUBMISSION REQUIRED UNDER 37 C.F.R. § 1.114**

- a. ☐ Previously submitted
- i. ☐ Please enter and consider the amendment(s)/reply under 37 C.F.R. § 1.116  
previously filed on \_\_\_\_\_
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on \_\_\_\_\_
- iii. ☐ Other \_\_\_\_\_
- b. ☒ Enclosed
- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statements (IDS)
- iv. ☒ Petition for Extension of Time
- v. ☐ Other \_\_\_\_\_

**2. MISCELLANEOUS**

- a. ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a  
period of \_\_\_\_\_ months
- b. ☐ Other \_\_\_\_\_

**3. FEES**

The USPTO is directed and authorized to charge the RCE statutory fee of \$790.00 and/or all required fees,  
except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments  
to said Deposit Account. A duplicate copy of this transmittal letter is attached.

**CORRESPONDENCE ADDRESS**

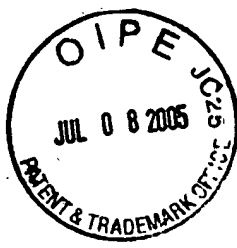
Direct all correspondence to the address for SUGHRUE MION, PLLC filed under the Customer Number listed below:

07/12/2005 MBEYENE1 00000051 194880 10669655  
01 FC:1801 790.00 DA

WASHINGTON OFFICE  
**23373**  
CUSTOMER NUMBER

**SIGNATURE OF ATTORNEY**

Name Daniel V. Williams Registration No. 45,221  
Signature *Daniel V. Williams* Date July 8, 2005



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q77597

Yasushi KINOSHITA

Appln. No.: 10/669,655

Group Art Unit: 2811

Confirmation No.: 5578

Examiner: Samuel A. GEBREMARIAM

Filed: September 25, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 C.F.R. § 1.114(c)**

**MAIL STOP RCE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 9, 2005, please amend the above-identified application as follows on the accompanying pages.

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